ESD24V88D-ULC

Transient Voltage Suppressors for ESD Protection

Description

The ESD24V88D-ULC is ultra low capacitance TVS designed to protect high speed data interfaces. This series has been specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from over-voltage caused by ESD (electrostatic discharge), CDE (Cable Discharge Events), and EFT (electrical fast transients).

Features

- ♦ 45 Watts Peak Pulse Power per Line (tp=8/20µs)
- Protects One Bidirectional I/O Line
- Low clamping voltage
- Low Capacitance
- Working voltages : 24V
- ◆ IEC61000-4-4 (EFT) 40A (5/50ηs)
- ◆ IEC61000-4-5 (LIGHTING) 1A (8/20µs)
- IEC61000-4-2 (ESD) ±15kV (air discharge) ±8kV (contact discharge)

Applications

- ♦ Cell Phone Handsets and Accessories
- RF Port
- Notebooks, Desktops, and Servers

Mechanical Characteristics

- Portable Instrumentation
- Industrial Controls
- Peripherals

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Functional Diagram



Mechanical Data

- SOD-882/DFN1006 (1.0x0.6x0.5mm) Package
- Molding Compound Flammability Rating : UL 94V-O
- Weight 0.5 Milligrams (Approximate)
- Lead Finish : Lead Free

Parameter	Symbol	Value	Units
Peak Pulse Power (tp=8/20µs waveform)	Ррр	45	Watts
Lead Soldering Temperature	TL	260 (10 sec.)	°C
Storage Temperature Range	Тѕтс	-55 to +150	°C
Operating Junction Temperature Range	TJ	-40 to +125	°C

ROHS

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Electrical Characteristics @ 25° C Unless Otherwise Specified)

Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Reverse Working Voltage	Vrwm				24	V
Reverse Breakdown Voltage	Vbr	IT=1mA;	26.4			V
Reverse Leakage Current	lr	Vrwm=24V, T=25°C;			0.1	μΑ
Clamping Voltage	Vc	Ι _{ΡΡ} =1 Α, ΤΡ =8/20μs;			45	V
Junction Capacitance	CJ	V _R = 0 V, f = 1MHz ;		0.25		pF

Characteristic Curves







Fig2. Power Derating Curve





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SOD-882/DFN1006 Package Outline & Dimensions



Ordering Information

Device	Marking	Package	Quantity	Reel Size
ESD24V88D-ULC	HF	SOD-882/DFN1006	10,000pcs/Reel	7 inch

Revision March 4 ,2024



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